



Advances in Analog and RF IC Design for Wireless Communication Systems: Chapter 7. Integrated Satellite Low Noise Block Down-converter

Pascal Philippe, Louis Praamsma, Marcel Geurts

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This chapter presents one of the first integrated Ku-band downconverters with PLL frequency synthesizer that has widely penetrated the market of low noise blocks (LNBs) for reception of satellite television. It starts with a brief description of the general architecture of a LNB and a presentation of the main requirements for the integrated downconverter and frequency synthesizer. The architecture of the integrated circuit and the design of its building blocks are then presented and discussed in detail. The emphasis is put on the design choices made in the downconverter and the synthesizer for competing in cost and performance with discrete implementations at minimal power consumption. By the end of the chapter, measurement results are reported that illustrate the downconverter performance alone or within a demonstration LNB.

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